

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Kowalski, et al. Attorney Docket: INF-132  
Filed: January 30, 2004 Examiner: TBD  
Serial No.: 10/766,902 Art Unit: 2818  
For: Test Structure for Improved Vertical Memory Arrays

Mail Stop: Missing Parts  
Commissioner for Patents  
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Alexandria, VA 22313-1450

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Dear Sir:

Applicants wish to bring to the attention of the Patent and Trademark Office the information noted on the enclosed form PTO/SB/08a that may be considered material to the examination of the above-identified application.

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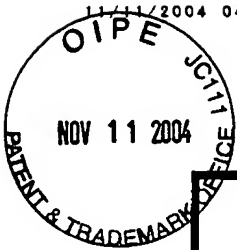
Respectfully submitted,

Ira S. Matsil  
Attorney for Applicants  
Reg. No. 35,272

November 11, 2004

Date

Slater & Matsil, L.L.P.  
17950 Preston Rd., Suite 1000  
Dallas, TX 75252  
(972) 732-1001 (phone)  
(972) 732-9218 (fax)



PTO/SB/DBa (08-03)

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				Application Number	10/766,902
				Filing Date	1/30/2004
				First Named Inventor	Kowalski, et al.
				Art Unit	2818
				Examiner Name	TBD
				Attorney Docket Number	INF-132
Sheet	1	of	1		

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Examiner Signature		Date Considered	
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